POZNAN UNIVERSITY OF TECHNOLOGY

EUROPEAN CREDIT TRANSFER AND ACCUMULATION SYSTEM (ECTS)

COURSE DESCRIPTION CARD - SYLLABUS

Course name
Edge AI and Computer Vision [S2Inf1-PB>AWIZ]

Course

Field of study
Computing

Year/Semester
1/2

Area of study (specialization)
Edge Computing

Profile of study
general academic

Level of study
second-cycle

Course offered in
Polish

Form of study
full-time

Requirements
compulsory

Number of hours

Lecture
30

Laboratory classes
30

Other
0

Tutorials
0

Projects/seminars
0

Number of credit points

5,00

Coordinators

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Lecturers

Prerequisites

Knowledge: Student starting this module should have a basic knowledge in the field of digital electronics and computer vision. Skills: The student should be able to obtain information from the indicated sources, as well as understand the need to expand his competences and be ready to cooperate in a team. Social Competences: The student should show such features as: honesty, responsibility, perseverance, cognitive curiosity, creativity, personal culture, respect for other people.

Course objective

- Providing students with knowledge related to selected issues of computer vision and image processing, with emphasis on hardware implementation for edge devices. - Familiarizing students with modern methods of designing, testing and prototyping devices performing image processing operations. - Developing students' skills in solving complex design problems in the field of edge vision applications. - Developing teamwork skills in students.

Course-related learning outcomes

Knowledge:
1. has advanced and in-depth knowledge of broadly understood it systems as well as methods and tools

used for their implementation, especially regarding building the hardware layer of vision systems.
2. has advanced detailed knowledge of selected issues in the field of computer science, in particular regarding the processes of image acquisition, image content analysis, and the construction of video processing systems.
3. has advanced and detailed knowledge of the processes taking place in the life cycle of it systems, especially the hardware layer of the systems.
4. knows advanced techniques and tools used in solving engineering tasks and conducting research in image processing systems, in particular regarding the methodology of conducting experiments and system evaluation.

Skills:
1. is able to combine knowledge from different areas of computer science (and if necessary from other scientific disciplines) to formulate and solve engineering tasks related to hardware-software development.
2. is able to assess the usefulness and the possibility of using new achievements (methods and tools) and new it products
3. is able to make a critical analysis of existing technical solutions and propose their improvements.
4. is able to assess the usefulness of methods and tools for solving an engineering task consisting in the construction or assessment of an it system or its components, including the limitations of these methods and tools.
5. is able to, in accordance with a given specification, design a complex device, it system or process and implement this project using appropriate methods, techniques and tools, including adapting existing or developing new tools for this purpose.

Social competences:
1. understands that knowledge and skills related to computer science quickly become obsolete,
2. understands the importance of using the latest knowledge in the field of computer science in solving research and practical problems.

Methods for verifying learning outcomes and assessment criteria

Learning outcomes presented above are verified as follows:

Learning outcomes presented above are verified as follows:
Formative assessment:
a) lectures: based on the answers to the questions which test understanding of material presented on the lectures
b) laboratory classes: based on the assessment of the tasks done during classes
Summative assessment:
a) in the field of lectures, verification of the assumed learning outcomes is carried out by the exam in electronic form on the Moodle platform, in case of doubt, the oral part of the exam is carried out;
b) verification of assumed learning objectives related to laboratories is based on:
- evaluating the report prepared partly during the course and partly after its completion; this assessment also includes teamwork skills;.
- assessing the implementation of a complex task requiring integrating knowledge and skills acquired during laboratory classes;
The final grade is determined using the following scale: (90%, 100%] -> 5.0, (80%, 90%] -> 4.5, (70%, 80%] -> 4.0, (60%, 70%] -> 3.5, (50%, 60%] -> 3.0, (0%, 50%]-> 2.0.
Getting extra points for activity during classes, especially for:
- proposing to discuss additional aspects of the issue,
- effectiveness of applying the acquired knowledge while solving a given problem,
- ability to work within a team that practically performs a specific task in a laboratory.

Programme content

The program covers the design of hardware accelerators. The use of reprogramming and hybrid systems in the analysis and processing of video signals.

Course topics

The lecture program includes the following topics:

Block 1:
- description, acquisition and presentation of video data
- elements of a modern vision system, standards, description methods,
- preparation of video data: removing distortions and interference from video signals before automatic analysis

Block 2:
- use of hybrid FPGA platforms to accelerate the processing of video streams
- hybrid device designs (fast on-chip interfaces: AXI, AXI-stream, DMA mechanism, NoC architecture)
- methods of testing and verifying prototypes of digital systems on programmable platforms: prototype verification scenarios, OCI (On-Chip Instrumentation), remote testing methods and in-system debugging (ISD)

Block 3: vision applications for edge devices (testing and implementing solutions using reconfigurable platforms; performance analysis for various hardware platforms).

Laboratory classes are conducted in the form of 2-hour meetings held in the laboratory, preceded by an instructional session at the beginning of the semester. The tasks are carried out by two-person teams of students. The laboratory program covers the following topics:
Implementation of typical computer vision applications on edge device hardware platforms. Implementation of solutions on CPU/VPU/FPGA platforms. Designing pipelined computational accelerators. Using the OCI technique to verify prototypes of APSoC/FPGA digital devices.

Some of the above-mentioned program content is carried out as part of the student's own work.

Teaching methods

1. Lecture with multimedia presentation (diagrams, formulas, definitions, etc.) supplemented by the content of the board.
2. Laboratory exercises: multimedia presentation, presentation illustrated with examples given on the board and performance of tasks given by the teacher - practical exercises.

Bibliography

Basic
1. Katarzyna Stąpor, Metody klasyfikacji obiektów w wizji komputerowej, PWN, 2011.
2. Sebastian Raschka, Vahid Mirjalili, Python: uczenie maszynowe, Helion, 2019.
3. Domański M., Obraz cyfrowy, WKŁ, Warszawa 2010.
Additional
1. Choraś, R. Komputerowa wizja. Metody interpretacji i identyfikacji obiektów. EXIT, 2006.
2. ITU-T Rec., H.264, Advanced video coding for generic audiovisual service, 2003.
3. N. SEBE, IRA COHEN, ASHUTOSH GARG, THOMAS S. HUANG, Machine Learning in Computer Vision,
Springer, 2005.

Breakdown of average student's workload

	Hours	ECTS
Total workload	125	5,00
Classes requiring direct contact with the teacher	60	2,50
Student's own work (literature studies, preparation for laboratory classes/ tutorials, preparation for tests/exam, project preparation)	65	2,50